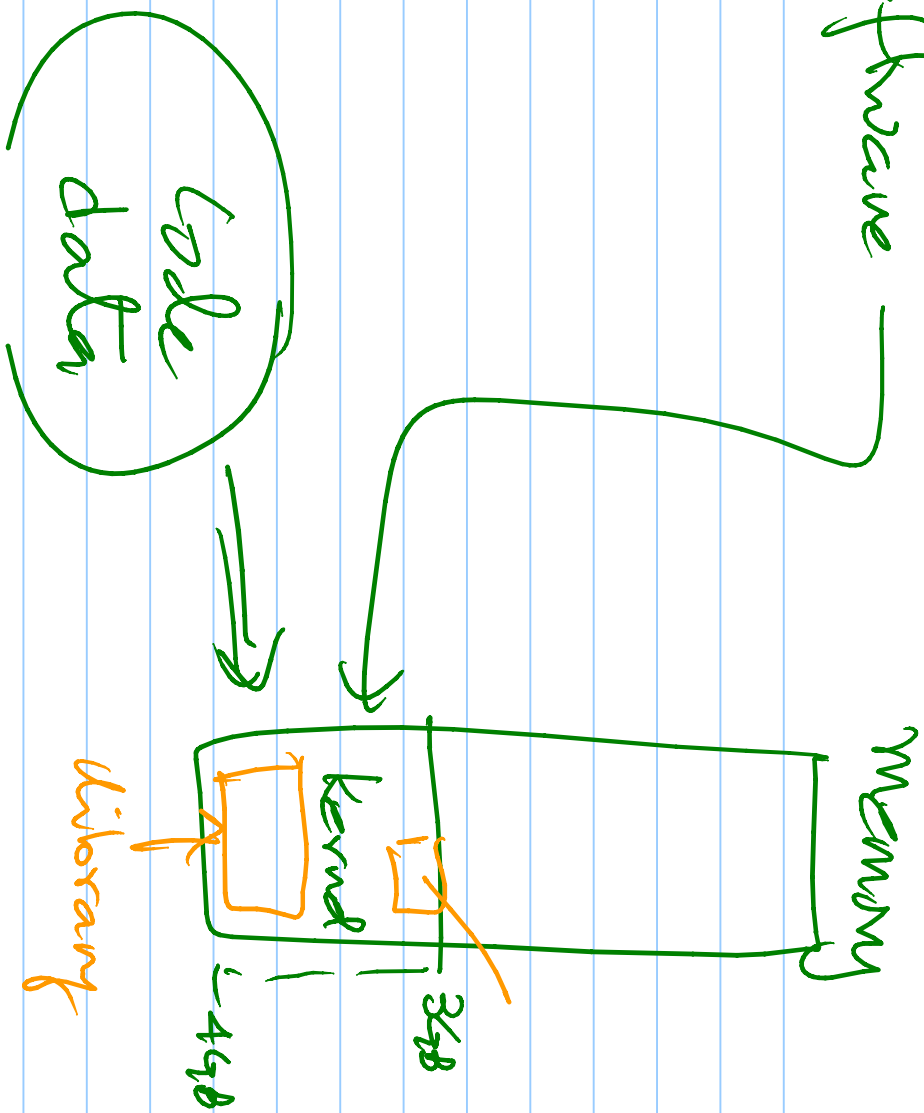


## Operating Systems

- CPU management → run multiple processes efficiently
- memory → allocate memory to processes
- devices

OS → Software



Software (OS) sees

CPU — single cycle

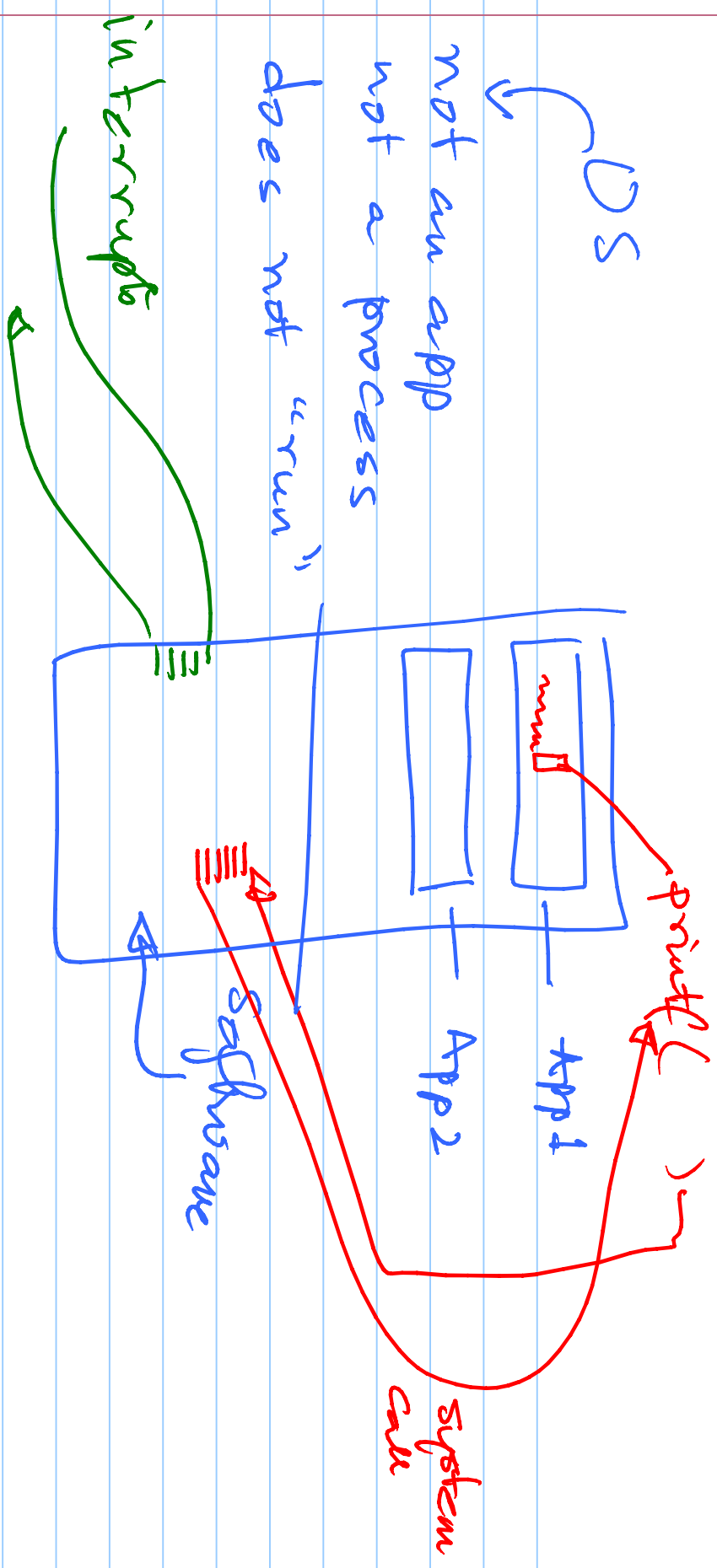
cache's — none

memory — RAM or virtual

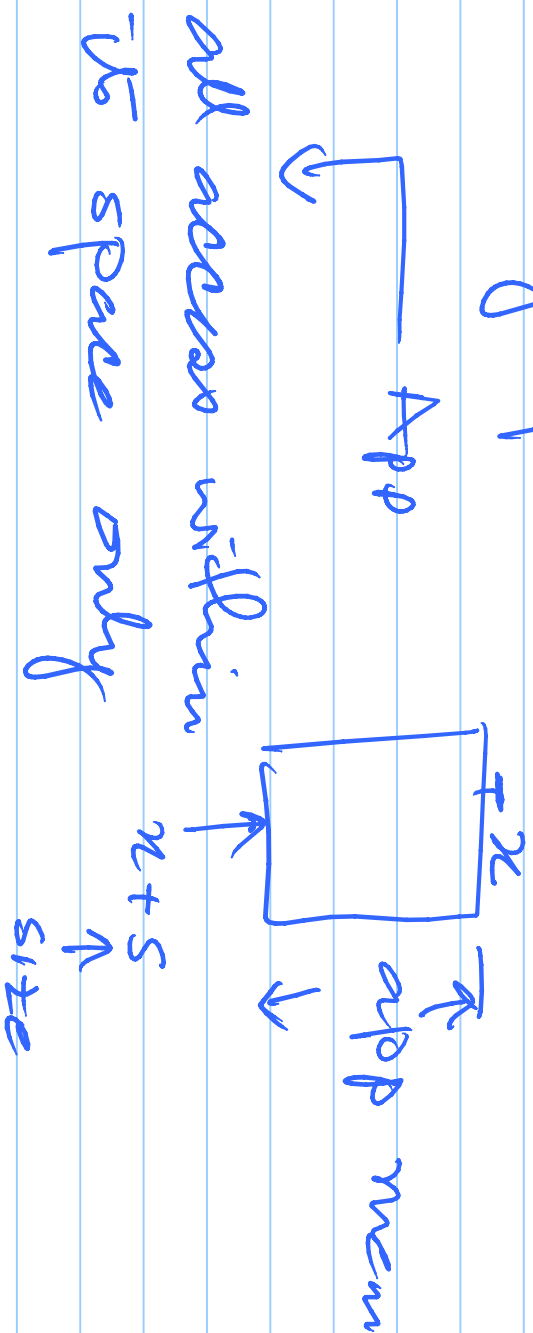
devices — ? ? ?

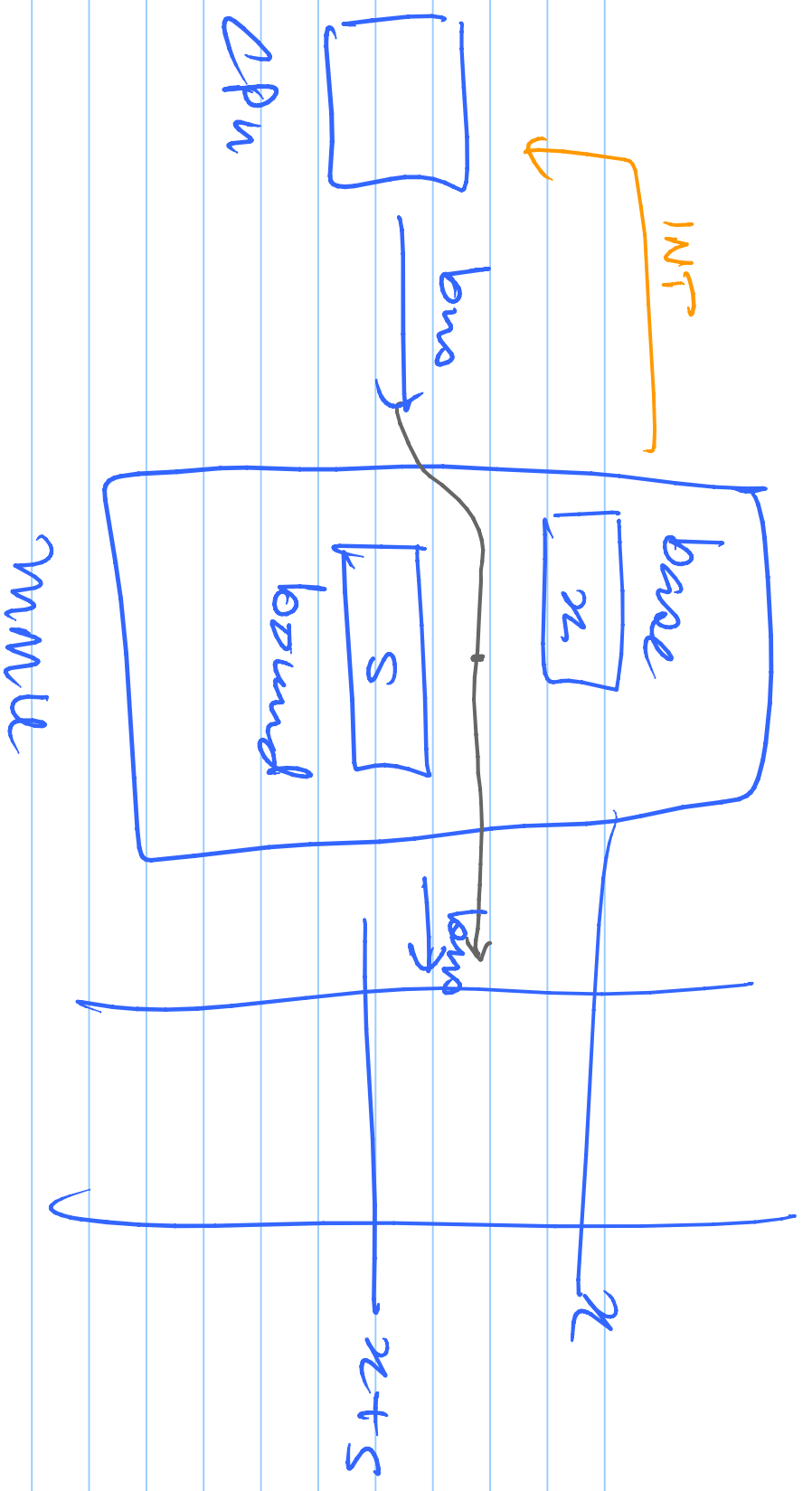
# Types of Operating Systems

- Desktop
- Server
- Batch
- Cluster
- Embedded
- Real Time → X



# Memory protection



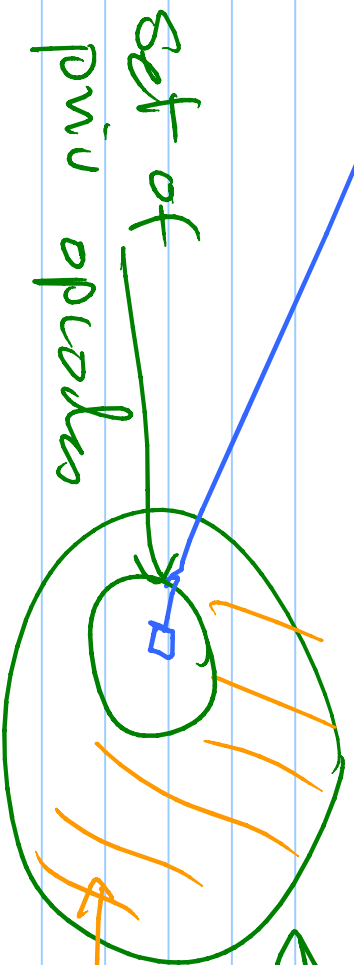


CPU modes (or rings)

↳ user mode

↳ privileged mode

change  
mmu  
wq



Set of  
priv ops

Set of  
all ops

priv  
mode

user mode

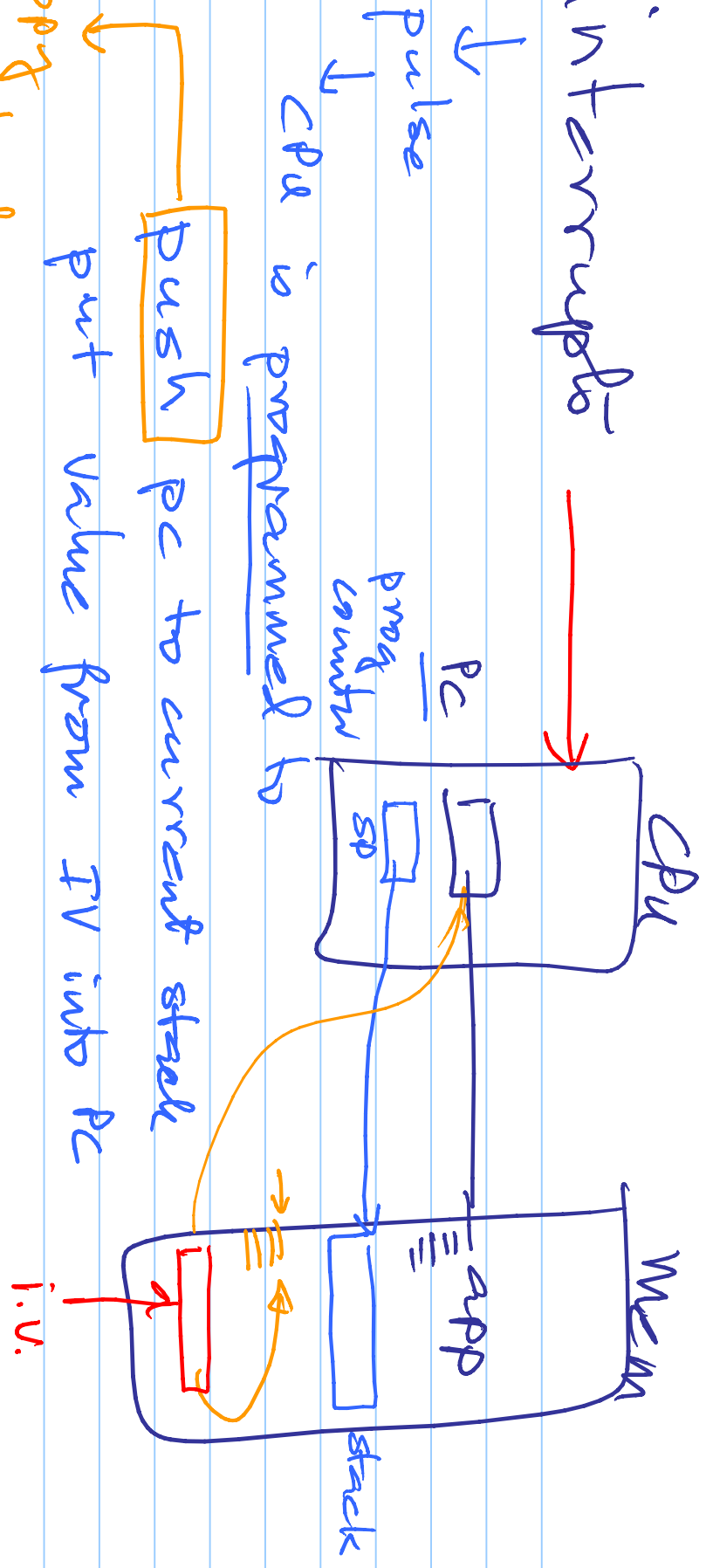
priv mode opcodes

→ to control register in mmio

→ to control data transfers  
in/out of devices

→ to control CPU thing  
e.g. timer

# Interrupts



- copy to stack
- dec SP

push

put value from IV into PC

CPU is programmed to

PC  
prog  
count

CPU

Mem

app

stack

I.V.

INT → jump to int handler routine

@ end of int handler

RTI → instruction

return from interrupt

↳ [ pop value from  
stack & put into PC

int a happens

CPU:

push PC to stack

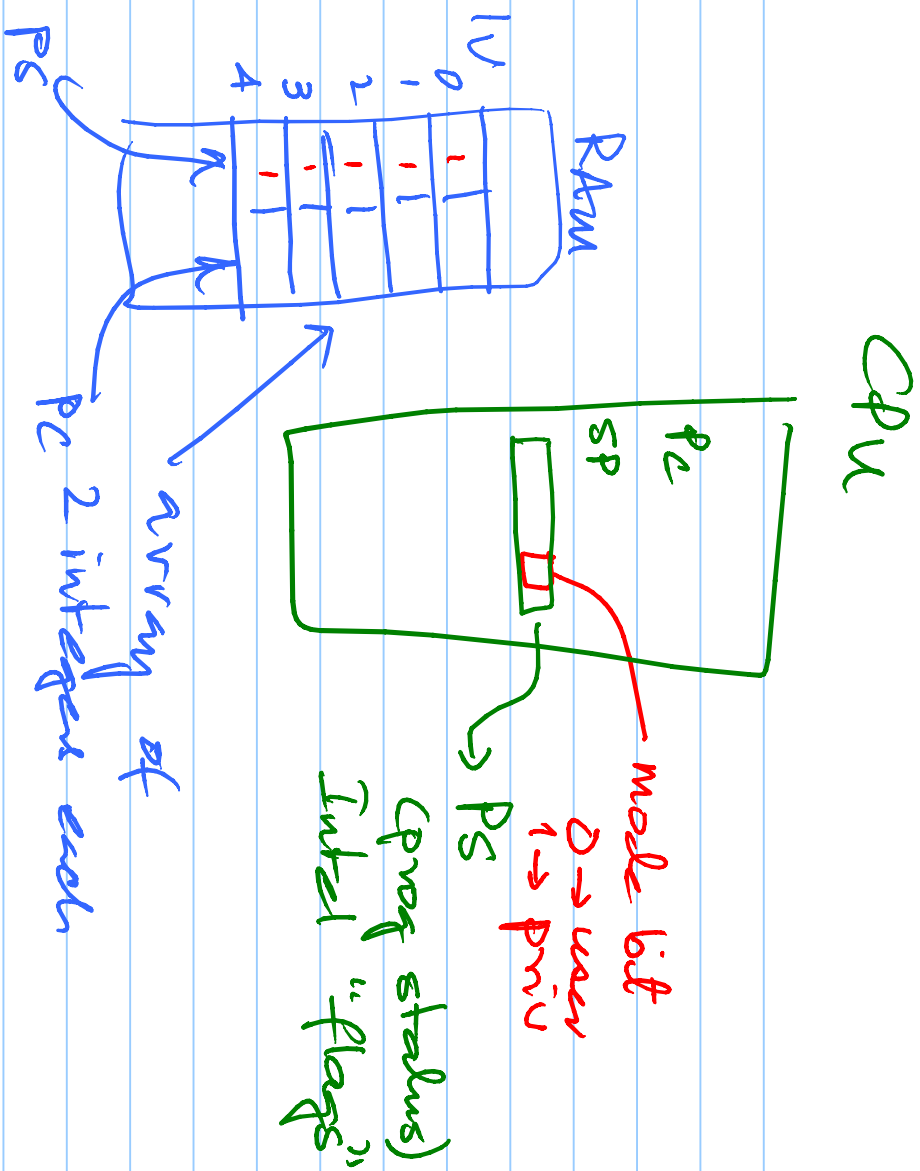
push PS " "

load PC from

$IV[x].PC$

load PS from

$IV[x].PS$



---

RTI  $\rightarrow$  pop stack  $\rightarrow$  PS  $\leftarrow$  mode = 0  
pop stack  $\rightarrow$  PC

