

- Scheduler (user level)

- locking & sync in user level

Scheduler

→ Semaphore (one option)

Semaphores



type  
variables that are global

→ sem\_t → struct { int count;  
                  q\_t q;  
                  }

Scheduler

→ start-fluxes  
→ yield  
→ (run)

Scan\_t s1, s2;

initScan(s, i) {  
    s.count = i;  
    initQ(s.q);  
}

P(s) → {  
    s.count--  
    if (s.count < 0)  
        { AddQ(s.q, DelQ(Runq))  
          X yield() }  
    else  
        { }  
}

→ [A] → [B] → [C]

AddQ(S.g, DelQ(RunQ))

~~field()~~

[B] → [C]

from = last TCB of S.g  
to = 1<sup>st</sup> TCB of RunQ  
suspend (from, to)

from = 1<sup>st</sup> frame in  
RunQ  
to → 2nd thread

rotate  
suspend (from, to)

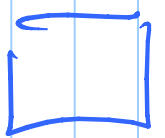
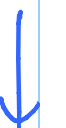
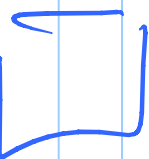
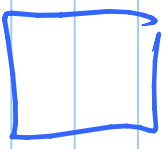
B ← C

Run8



delete

add



TCB

TCB

TCB



FRONT



BACK

$V(s)$

{ s.count ++

if (s.count <= 0)

{ AddQ(RearQ, DelQ(s-q)) }

→ yield() // not needed?

}

A

loop

init = 1

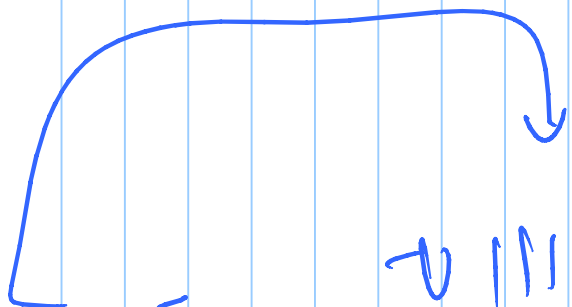


≡

P(mutex)

CS

V(mutex)



B

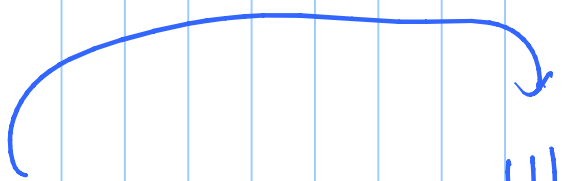
loop

≡

P(mutex)

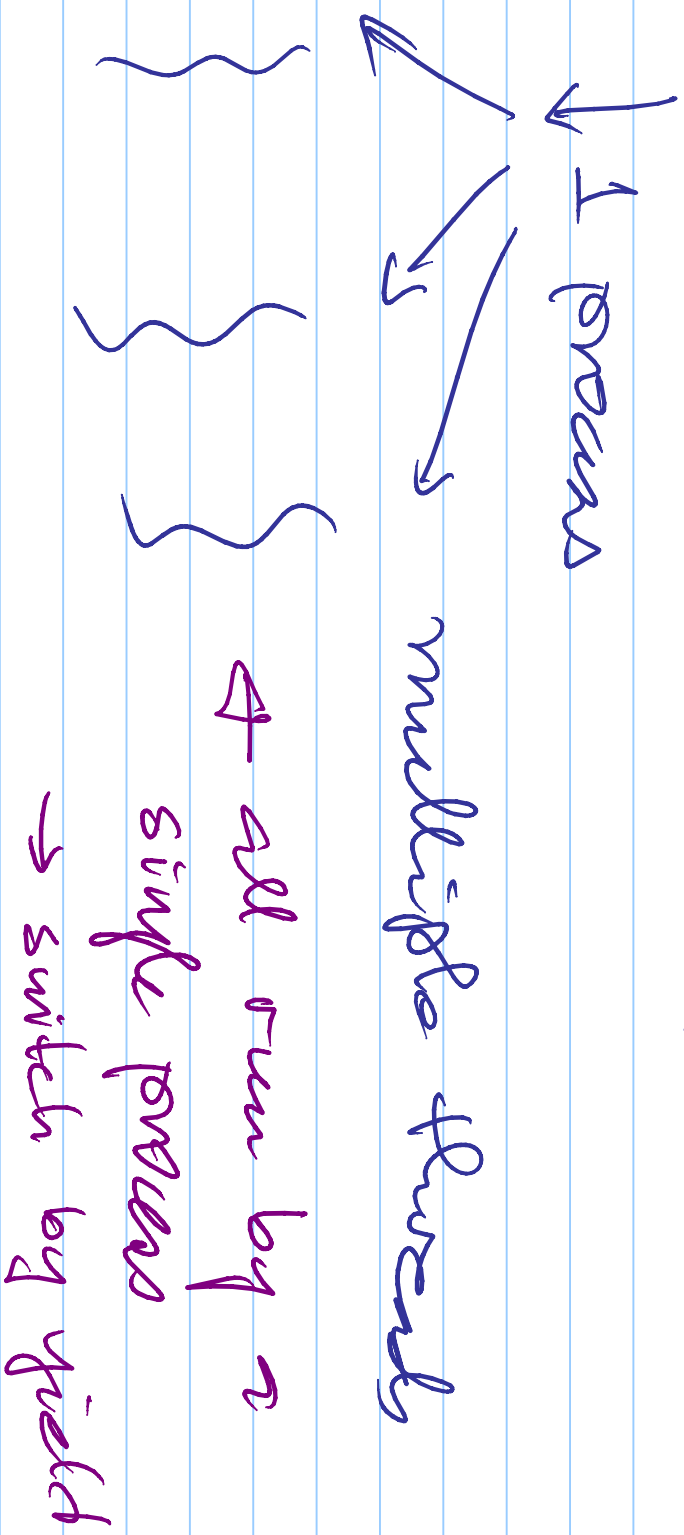
CC

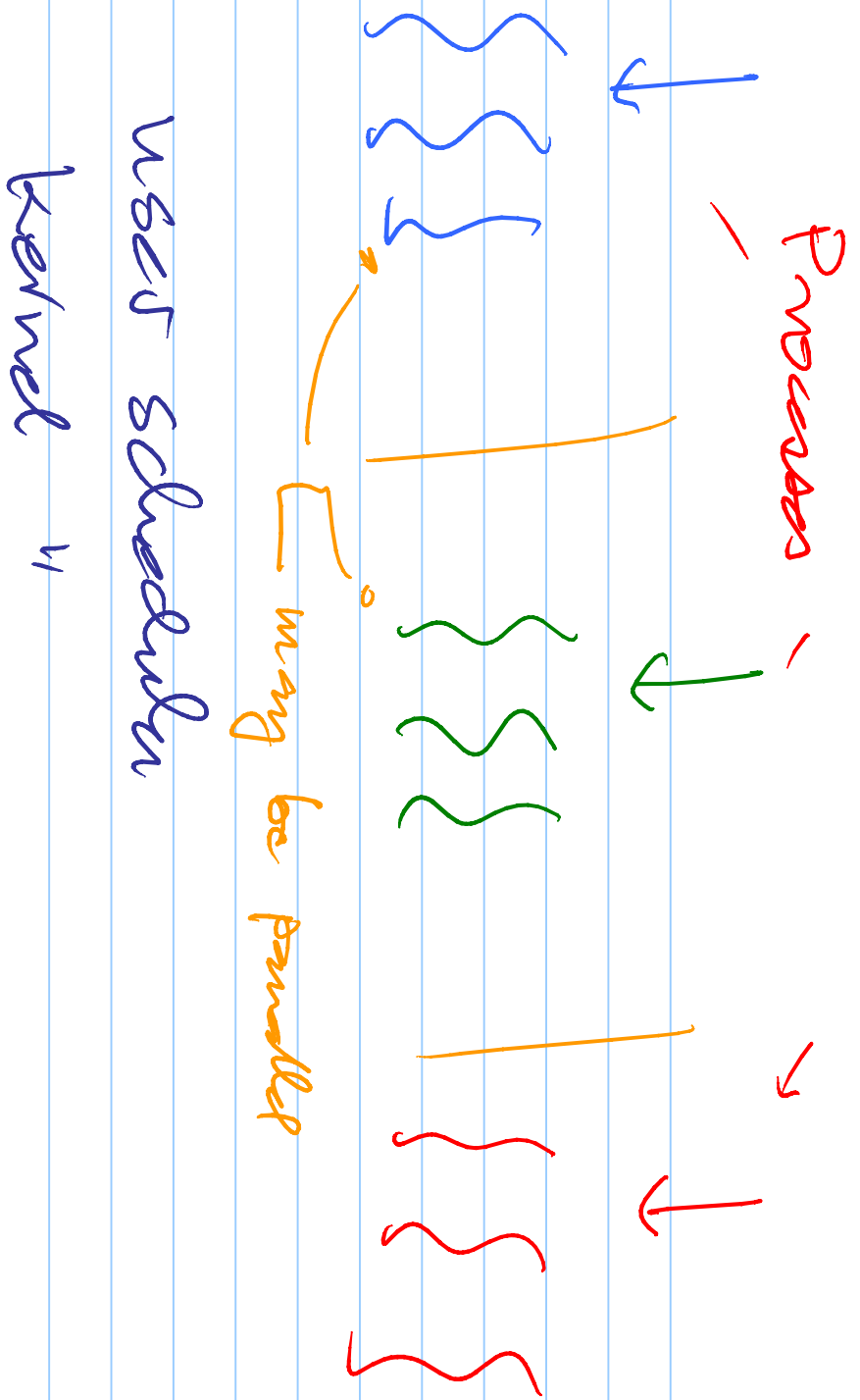
V(mutex)



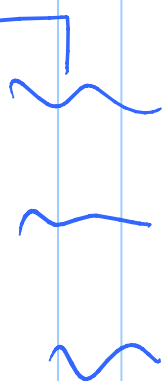
- Code should be written with assumption threads are preempted
- Critical sections needed, use semaphores init to 1
- synchronization → use sem
- no 'yield' necessary

# User level scheduling





1



system call

→ block (zfo wait)

# User level threads

- 1 process ↓ in kernel
- if a thread blocks all threads block
- No parallel execution → even on multiprocessors
- fast switching, no kernel calls

# kernel level threads

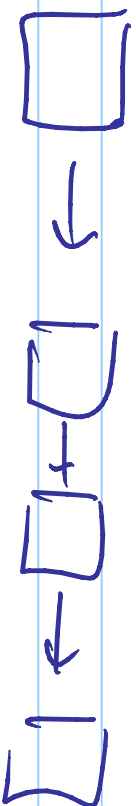
- 1 scheduler in kernel
- start thread (or pthread\_create or clone) → system call
- creates TCB in kernel
- add to run Q
- use same address space as parent

- multiple threads of same application can run in parallel
- thread  $\rightarrow$  system call  $\rightarrow$  block  
blocks only the thread & not other threads

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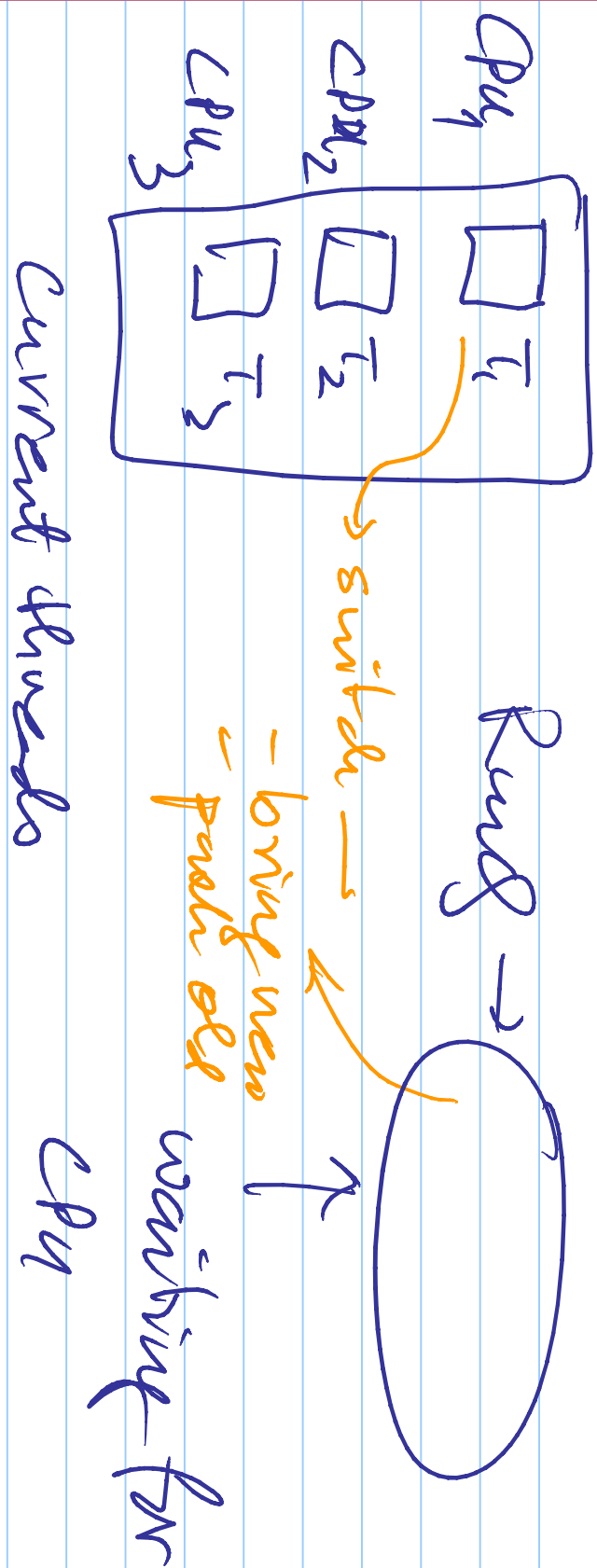
# kernel scheduler (uniprocessor)

↓ Run

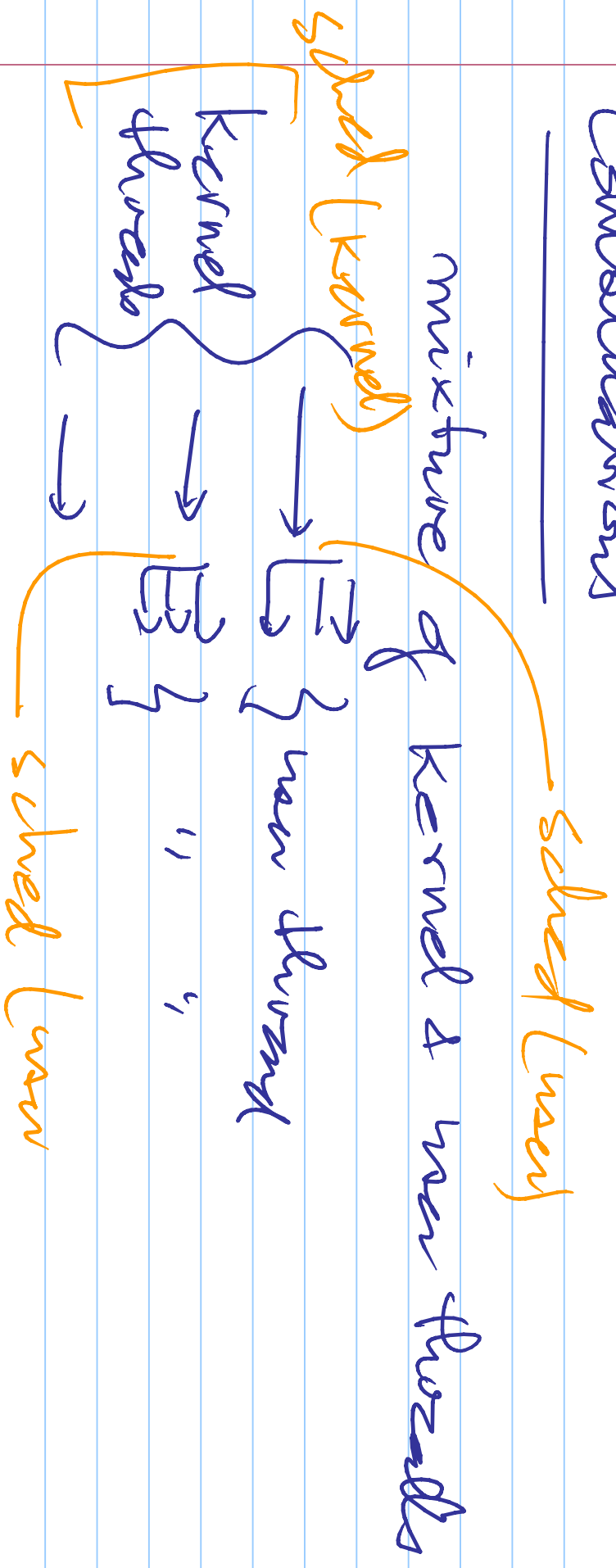


- yield = same, but in kernel  
→ called on interrupt
- semaphores → in kernel

# Multicon Scheduling



# Combinations



1x N scheduling - 1 kernel thread

runs N user  
threads

- 1 process may ~~be~~ have  
multiple kernel threads

$M \times N$  scheduling

user kernel CPU  
 $N > M$   
100 10 3

in each process

→  $M$  kernel threads

→  $N$  user threads

→ 1 scheduler

Tasks / Process  $\rightarrow$  thousands (kernel)

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↑ many diff